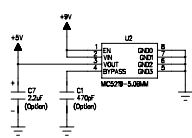


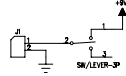
MAIN BOARD

REVISION RECORD			
LT#	ECO NO	APPROVED	DATE

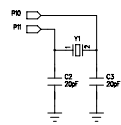
[Regulator from Battery Voltage input to Power]



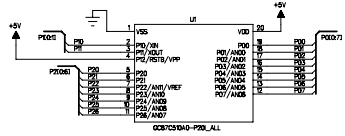
[Battery Interface]



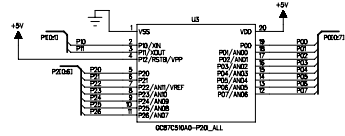
[External Clock]



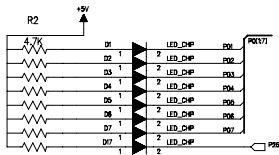
[MIDAS 11 Family : SOC-20P1]



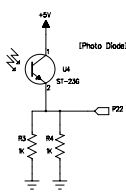
[MIDAS 11 Family : SPDP-20P1]



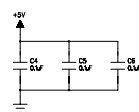
[Chip LED Part]



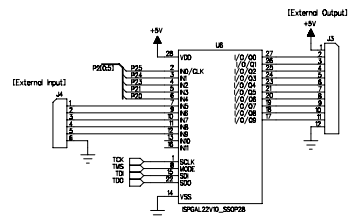
[ADC Input for Light Sensor]



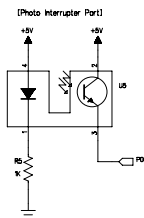
[Bypass Capacitor]



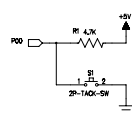
[Lattice SPLD Device]



[Interrupt 0 Input : P00 (INT0)]



[Push-button Part]



[Interface for SPLD Device]



Informational

PCB Layer : 2 Layers
PCB Thick : 16μ
Designed by Hyun_lee

Latest Modified Date :
V1.0 : Wed, April 28, 2004

Descriptions :
1. MIDAS 11 Family Demo Board
2. Additional Test Board
3.

E-mail : mcu-support@coreriver.com

DRAWN	CHKD.	CHKD.	APPD.	TITLE	HERA
				HERA Application Board	
				CODE NO.	
				MODEL NO.	MAIN BOARD
				SHEET	1 / 1 REV. 1.0
CORERIVER Technology				DATE	Wed, April 28, 2004